The SIAM Conference on Parallel Processing for Scientific Computing (SIAM PP) February 14th, 2020 | Seattle, Washington, USA

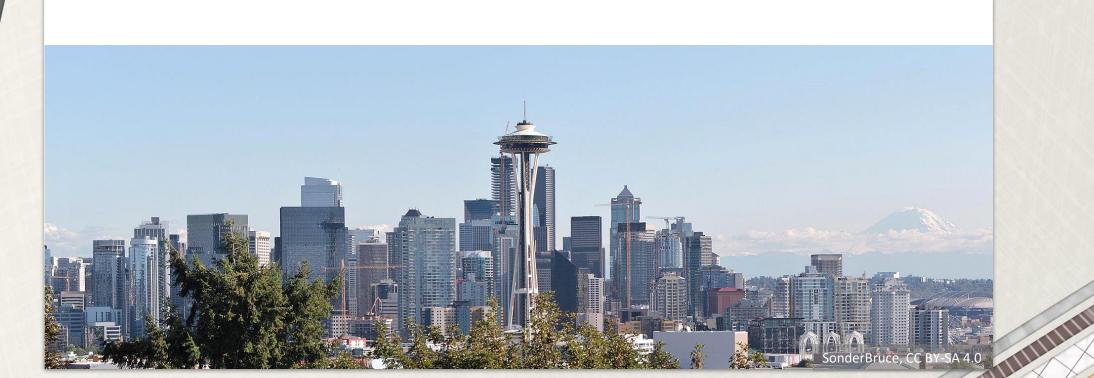
# **Approximate and Exact (Multi-)Selection on GPUs**

<u>Tobias Ribizel</u><sup>1</sup>, Hartwig Anzt<sup>1,2</sup> <sup>1</sup>Karlsruhe Institute of Technology <sup>2</sup>University of Tennessee



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HPC-Seminar – FAU Erlangen 23.03.2021

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# Approximate and Exact (Multi-)Selection on GPUs

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## **Selection Problem**

Given an unsorted sequence of real numbers  $x_0, x_1, x_2, x_3, \ldots x_{n-1}$ , we want to find the element  $x_{i_k}$  such that in the sorted sequence

$$x_{i_0} \le x_{i_1} \le x_{i_2} \le x_{i_3} \le \dots \le x_{i_k} \le \dots x_{i_{n-1}}$$

the element  $x_{i_k}$  is located in position k.

We do not necessarily need to sort the complete sequence!

- Statistics (Quantiles)
- Top-*k* selection
- Thresholds

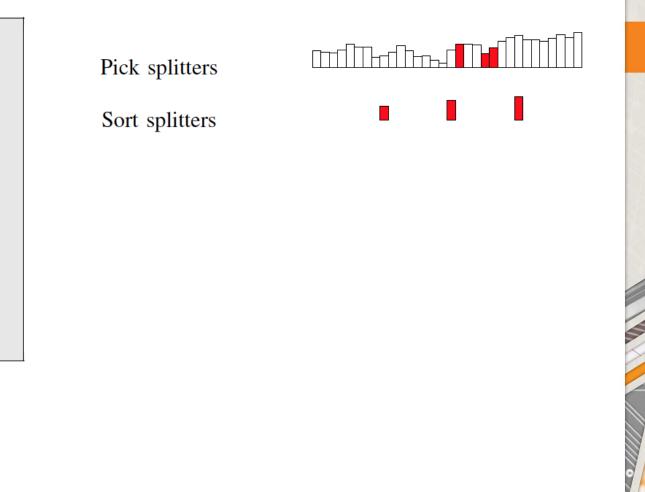


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       if (size(data) <= base_case_size) {</pre>
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           sort(data);
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           return data[rank];
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       // select splitters
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       splitters = pick_splitters(data);
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       // compute bucket sizes n_i
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       counts = count_buckets(data, splitters);
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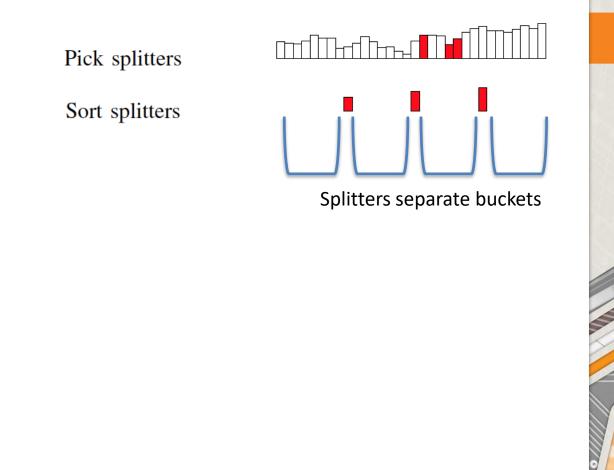
Pick splitters



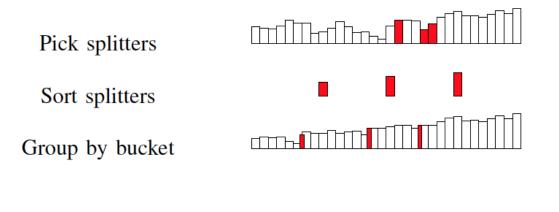
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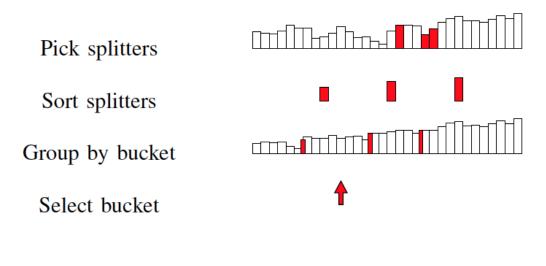
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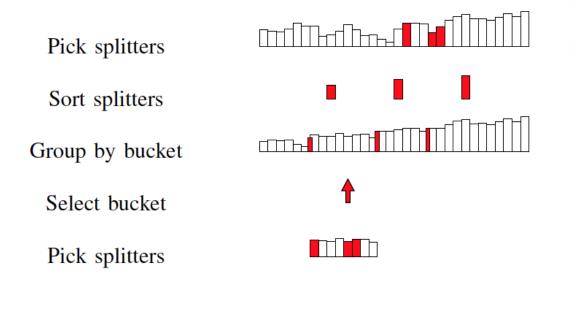
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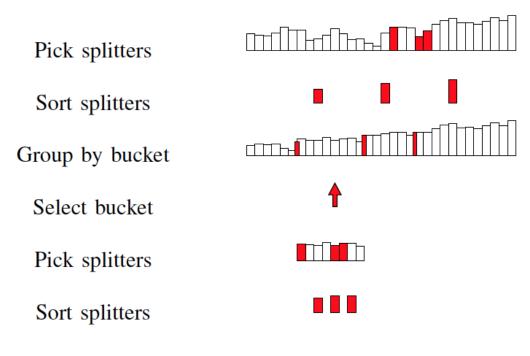
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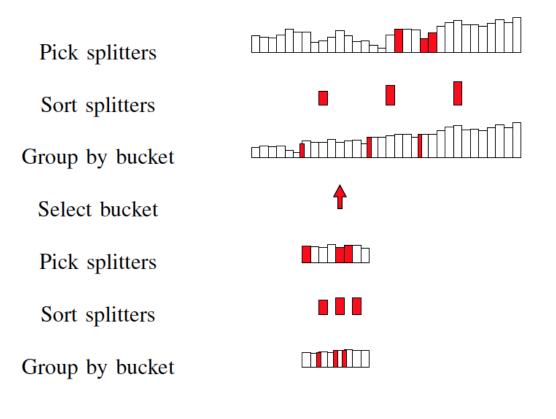
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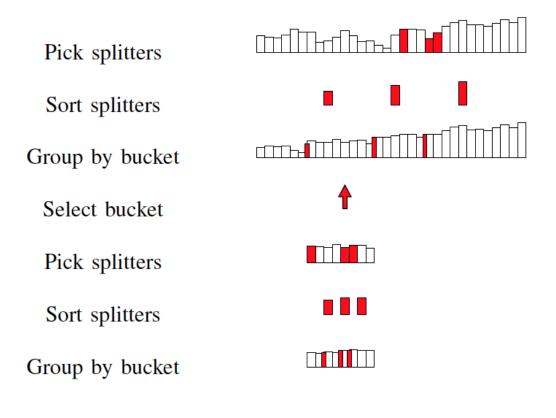
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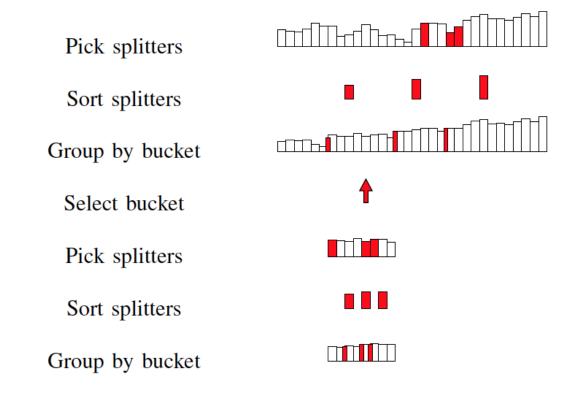
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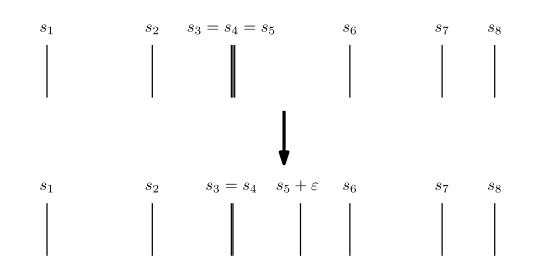
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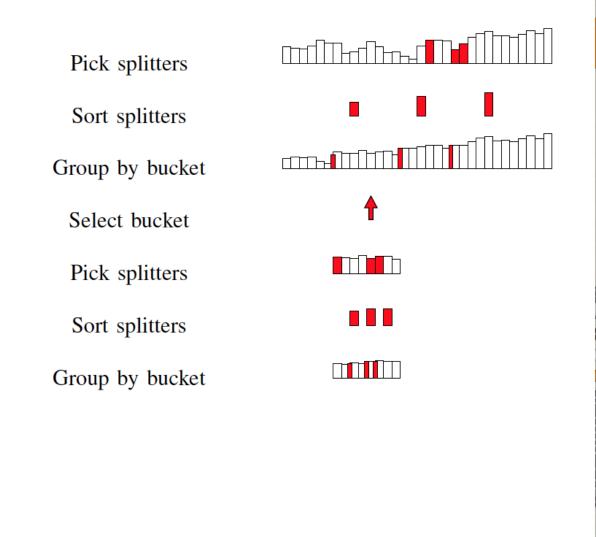


• We only copy elements of the buckets we are interested in;

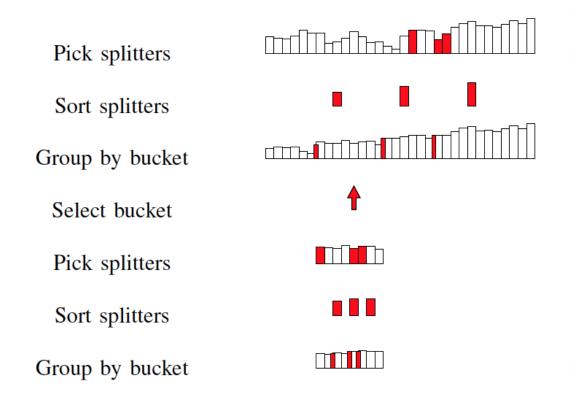


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- In case of identical splitter elements, they are placed in an equality bucket;
- If target rank is in an *equality bucket*, the algorithm can terminate early by returning lower bound;

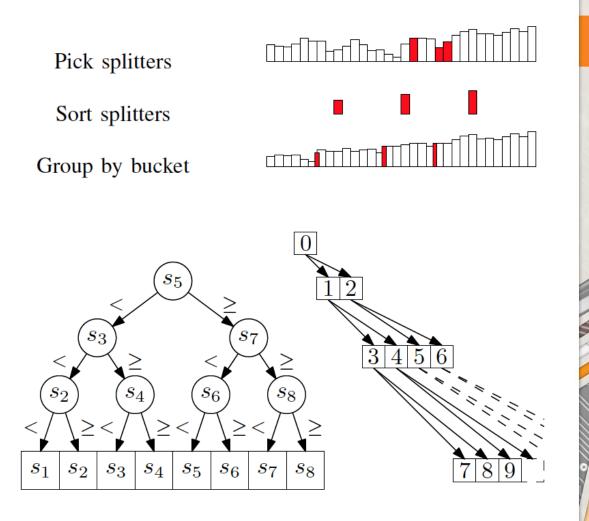




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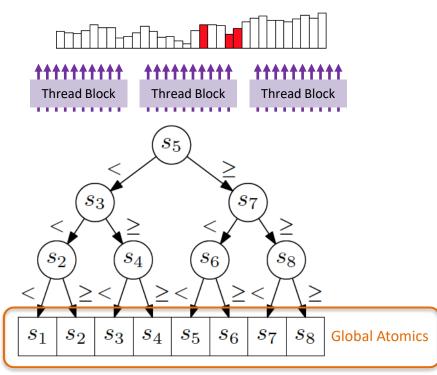


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- If target rank is in an *equality bucket*, the algorithm can terminate early by returning lower bound;
- For sorting the splitters, small input datasets, and the lowest recursion level a *bitonic sort* in registers + shared memory is used;
- Use a *binary search tree* to sort elements into the buckets;
- Store the bucket indices to avoid recomputation (also helpful for kernel fusion)



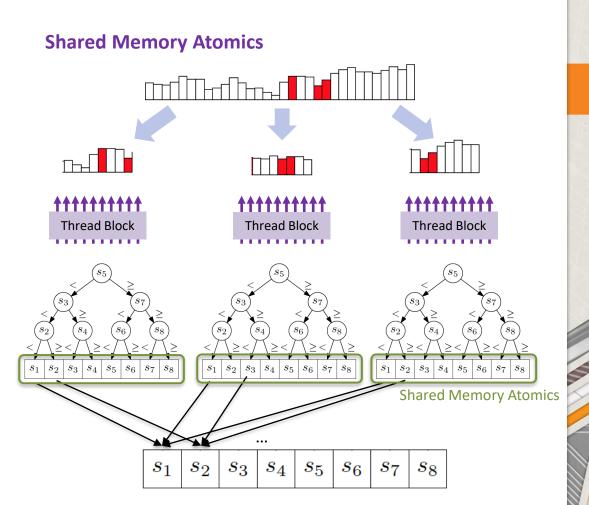
# **Parallelization & Communication**

#### **Global Memory Atomics**



• Run SampleSelect using all resources on complete data set;

• Use global atomics to generate bucket counts;



- Split data set into chunks, assign to thread blocks;
- Each thread block runs bucket count on its data;
- Use a global reduction to get global bucket counts;

### **Experiment Setup**

- 2 distinct GPU architectures
- Input datasets with 2<sup>16</sup> to 2<sup>28</sup> elements
- *d*= 1, 16, 128, 1024, *n* distinct values
- All results averaged over 10 runs
- Single precision input data
- Comparison against QuickSelect kernel
- QuickSelect and SampleSelect have same performance optimization level
- Correctness check using C++ std::nth\_element

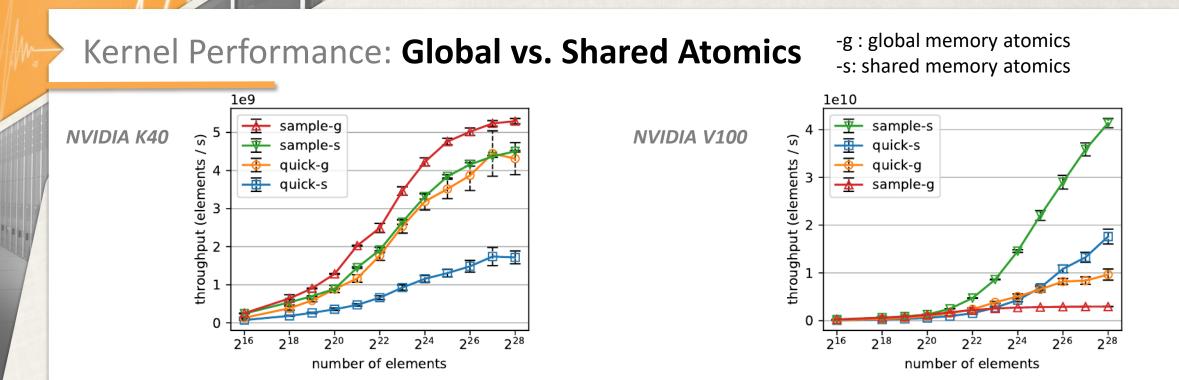
	NVIDIA K40	NVIDIA V100
Architecture	Kepler	Volta
DP Performance	1.2 TFLOPs	7 TFLOPs
SP Performance	3.5 TFLOPs	14 TFLOPs
HP Performance	_	112 TFLOPs
SMs	13	80
Operating Freq.	0.75 GHz	1.53 GHz
Mem. Capacity	5 GB	16 GB
Mem. Bandwidth	208 GB/s	900 GB/s
Sustained BW	146 GB/s	742 GB/s
L2 Cache Size	1.5 MB	6 MB
L1 Cache Size	64 KB	128 KB
	2013	2017



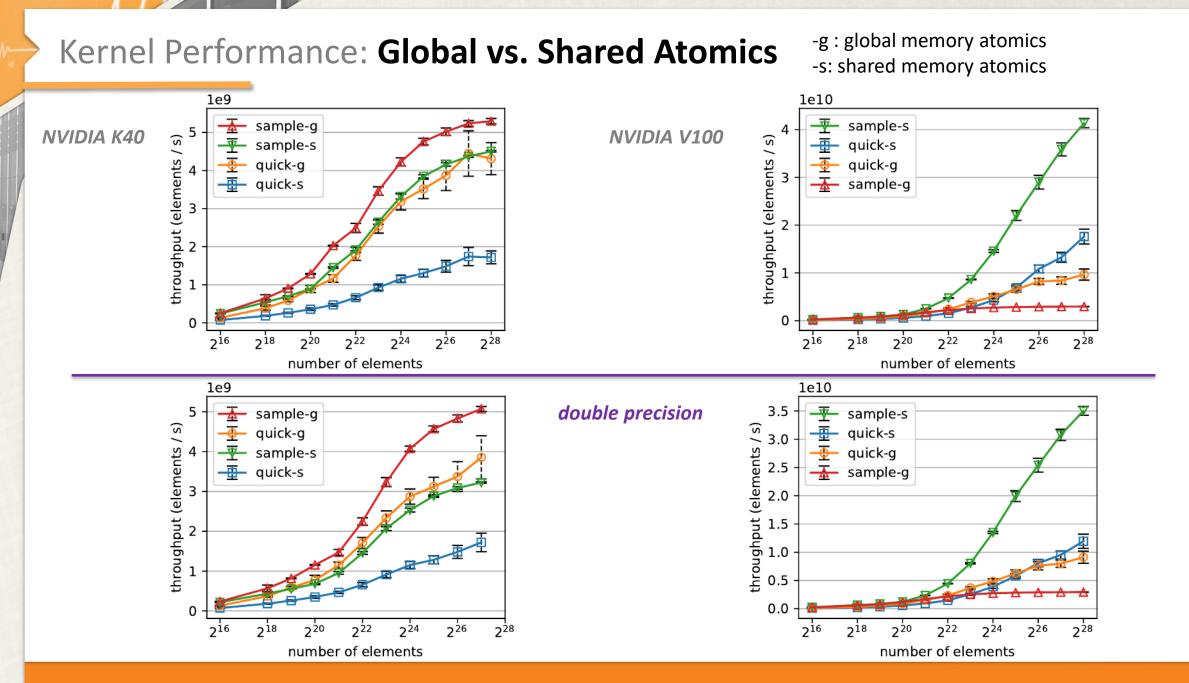
#44@TOP500



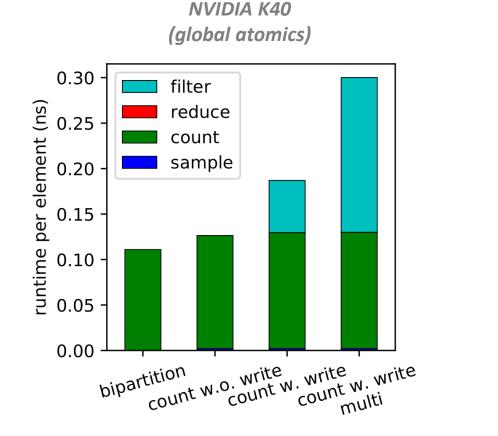
#1@TOP500



Larger performance variation for QuickSelect as we are more likely to run into the "Worst-Case" performance.



### **Runtime breakdown**



**NVIDIA V100** (shared atomics)

tition count w.o. write count w. write count w. write multi

filter

reduce

count

sample

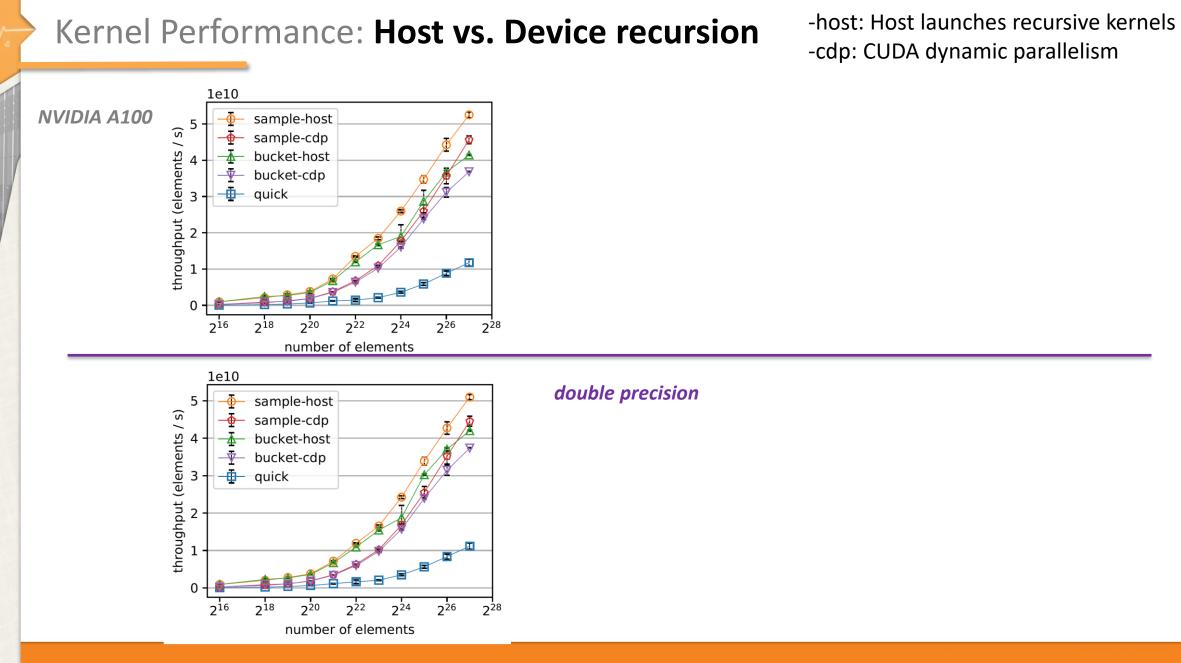
0.08 -

runtime per element (ns) 700 000 000 700 000 000

0.00

bipartition

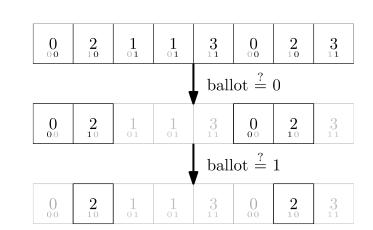
 $n = 2^{24}$ , single precision



N

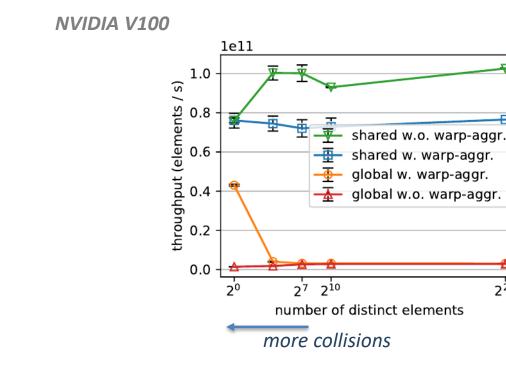
# Kernel Optimization: Element Repetition

Idea: use warp aggregations to mitigate the performance impact from atomic collisions.

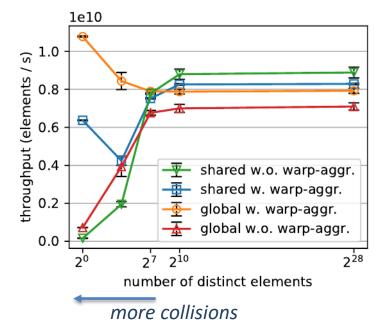


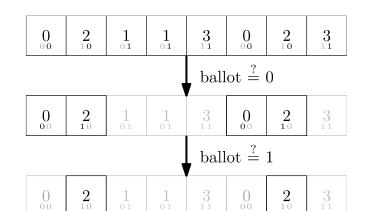
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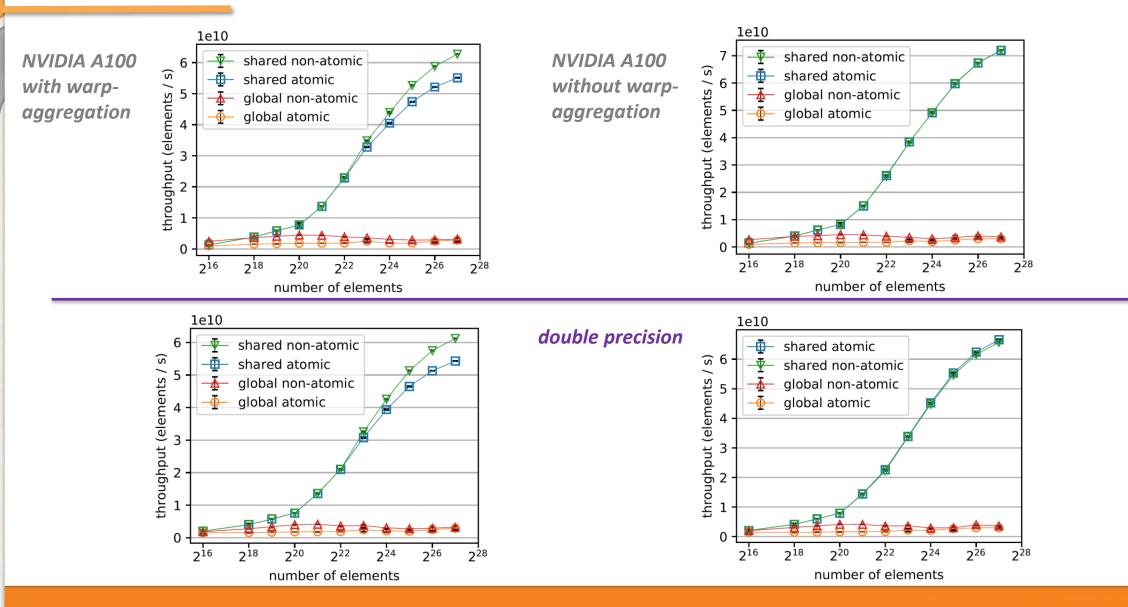
#### NVIDIA K40





228

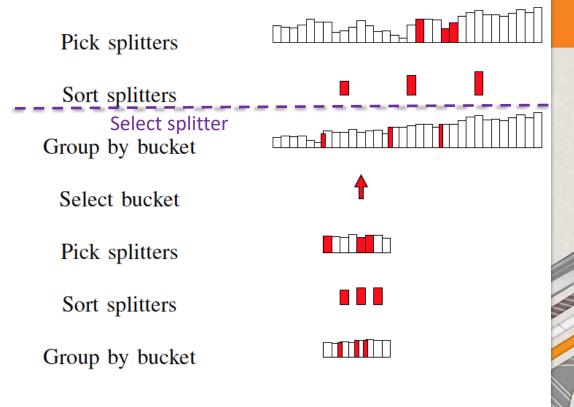
# Kernel Performance: What if we needed no atomics?



### **Approximate Selection**

We do not descend to the lowest level of the recursion tree, but limit to one single bucket selection.

- Accuracy depends on the number of splitters vs. dataset size
- Accuracy independent of value distribution (works on ranks, only)



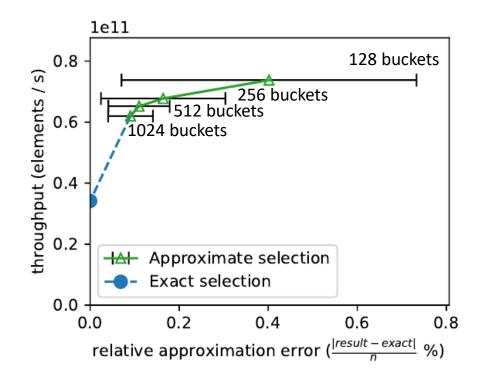
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Test problem:

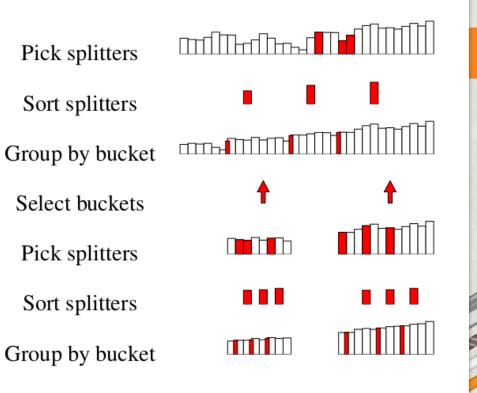
- 2<sup>28</sup> uniformly distributed single precision values
- Approximate selection uses 1 level only
- We report statistics over 10 runs



# **Multiple Selection**

**Generalization:** Select elements at *multiple ranks*  $k_1, ..., k_m$  simultaneously

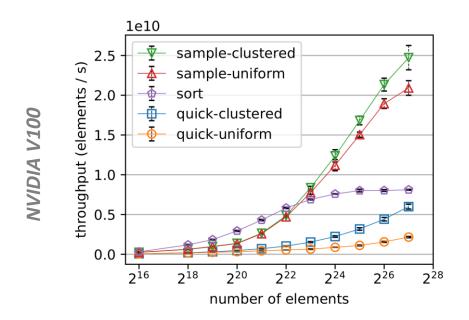
- Determine which buckets contain  $k_i$  using binary search
- Extract elements from all these buckets simultaneously
- Launch multiple subcalls using CUDA *dynamic parallelism*



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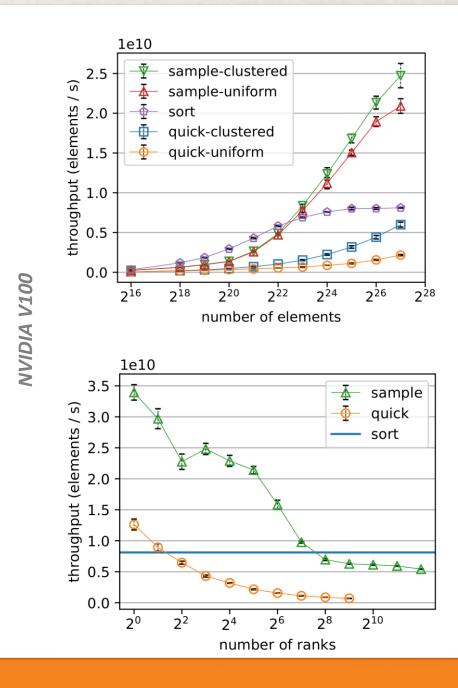
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- Launch multiple subcalls using CUDA *dynamic parallelism*
- Comparison with *QuickSelect* and CUB *RadixSort*
- Input ranks: *clustered* with  $k_i = 2^i$  (best case) *uniform* with  $k_i = \frac{i}{32} \cdot n$  (worst case)



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- Comparison with *QuickSelect* and CUB *RadixSort*
- Input ranks: *uniform* with  $k_i = \frac{i}{\#ranks} \cdot n$  for  $n = 2^{27}$



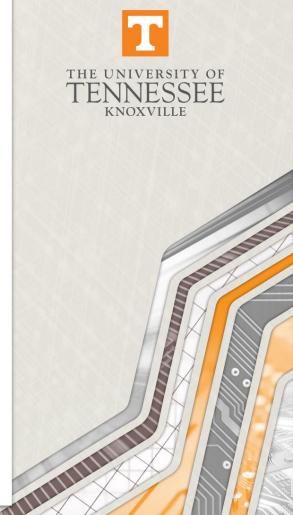
### **Summary and Outlook**

- SampleSelect kernel much faster than QuickSelect
- 36% (single) 48% (double) of experimental peak memory bandwidth on NVIDIA V100
- Approximate selection >2x faster than exact selection
- Multiple selection faster than sorting for up to 128 ranks

From a performance engineering standpoint (overgeneralized take-aways <sup>(2)</sup>):

- Hardware support beats warp-aggregation for atomics
- Shared-memory atomics are blazingly fast
- Host-side kernel launches outperform dynamic parallelism for tail recursion
- Pruning your recursion tree can be worthwile (if you still have enough parallelism left)







Helmholtz Impuls und Vernetzungsfond VH-NG-1241

### References

- 1. T. Ribizel and H. Anzt, "Approximate and Exact Selection on GPUs," Proceedings of the 9th AsHES Workshop at IPDPS, 2019
- 2. T. Ribizel and H. Anzt, "Parallel selection on GPUs," Parallel Computing, vol. 91, p. 102588, Mar. 2020



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